

<b>Notice of References Cited</b>	Application/Control No. 10/782,672		Applicant(s)/Patent Under Reexamination MCINTOSH, GORDON D.	
	Examiner Leynna T. Truvan		Art Unit 2135	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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#### FOREIGN PATENT DOCUMENTS

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	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Vounckx, Johan: Reconfiguration and Fault-tolerant Routing in Massively Parallel; 1996, Belgium
	V	Govindarajan-S-C, Mehendale-M, & Govindarajan-R: Area and Power Reduction of embedded DSP Systems using Instruction Compression and Re-configuration Encoding; Journal of VLSI Singal Processing Systems for Signal Image and Video Technology; Kluwer Academic Publishers, Netherlands; 2006
	W	Vogt-T & Wehn-N; A reconfiguration Application Specific Instruction Set Procesor for Viterbi and Log-MAP Decoding; 2006 IEEE Workshop on Signal Processing Systmes Design and Implementation; IEEE Psicataway, NJ; 2006
	X	Quisquater-J-J & Samyde-D: Automatic Code Recognition for Smart Cards Using a Kohonen Neural Network; Usenix Assoc, Berkeley, CA; 2002

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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